



# MIL-PRF-38535 Standard Microcircuits Hermetic and Non-hermetic

June 14, 2021

**Shri G. Agarwal**

NASA – Jet Propulsion Laboratory,  
California Institute of Technology  
Shri.g.agarwal@jpl.nasa.gov  
818-354-5598

NEW science INVESTIGATIONS and technology EXPERIMENTS are leading the return to the Moon beginning in 2021. Through a variety of upcoming robotic and human activities on the surface and in orbit around the Moon, we will better understand the universe and our home planet.

Image Credit: NASA

- The mission assurance organizations at NASA have supported many space missions/programs, large and small. Today, that spectrum has got wider, ranging from smallsats/cubesats to flagship missions such as the planned Europa mission. As always, the success of each and every mission counts.
- This presentation is about infusion of new technology into the standards for microcircuits, and the work underway to meet the needs of new missions.



# Partnering with the Community



## JEDEC JC-13 (Manufacturers)

JC-13	Solid State Devices for Government Products
JC-13.1	Discrete Semiconductors for Government Products
JC-13.2	Microelectronics for Government Products
JC-13.4	Radiation Hardness
JC-13.5	Hybrids and Multi-chip Modules for Government Products
JC-13.7	New Electronic Device Insertion for Government Products

## SAE CE-11/CE-12 (Industry Users, Primes, Subs)

SAE SSTC CE-11	Users of Passive Components
SAE SSTC CE-12	Users of Solid State Devices
<b>CE-12 Management:</b>	
<b>Chair – A. Touw</b>	
<b>Vice Chair – (JPL) S. Agarwal</b>	
SAE SSTC CE-11 & CE-12	Space Subcommittee Chair – S. Agarwal

Joint meetings held 3 times a year



**NASA Centers:**

ARC	JSC
GRC	KSC
GSFC	LaRC
JPL	MSFC

**Partners from Outside NASA:**

Domestic  
JHU/APL, Others  
The Aerospace Corp,  
U.S. Air Force, U.S. Navy,  
U.S. Army, DLA,

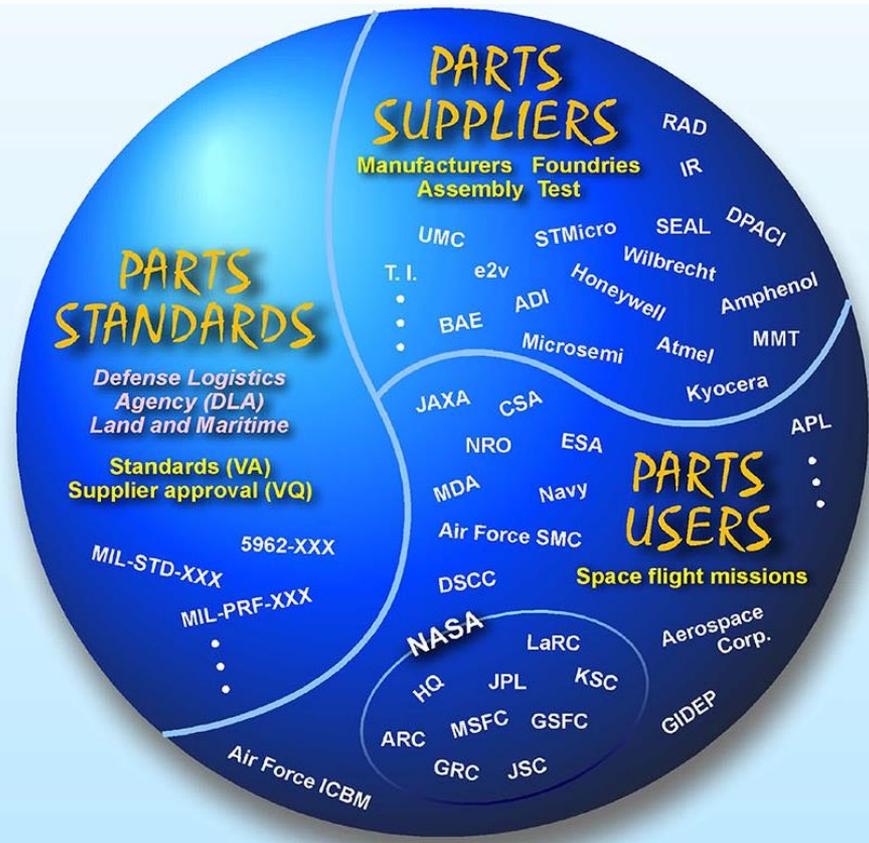
International  
ESA, JAXA, CSA

Weekly NEPAG and Biweekly GWG Telecons (Domestic)

Monthly Telecons (International and HWG)

# Space Parts World

## Developing/Maintaining Standards for Electronic Parts

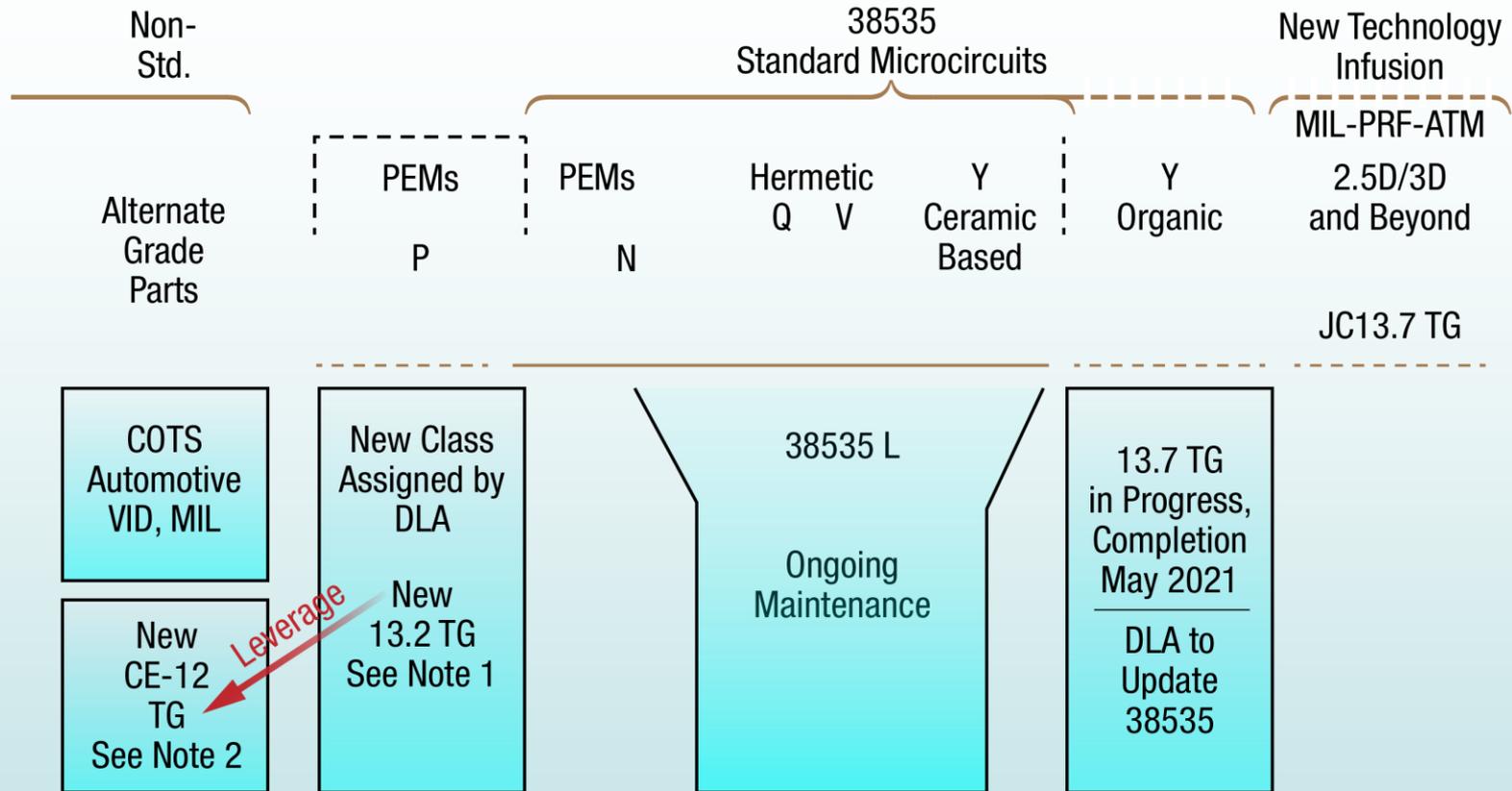


The parts users and standards organizations work with suppliers to ensure availability of standard parts for NASA, DoD, and others. **For Space microcircuits, DLA, NASA/JPL (S. Agarwal\*) and the U.S. Air Force / Aerospace Corp. (L. Harzstark) form the Qualifying Activity (QA).**

\*Also Systems, Standards and Technology Council (SSTC) G-12 Vice-Chair; Chair, Space Subcommittee.

# Options for Microcircuits

Rev. D, 6-10-21



- Note 1: Standard PEMs for Space (QMLP) initiative using SAE AS6294 as baseline. Supported by NASA Parts Bulletins on PEMs.
- Note 2: For alternate grade microcircuits, follow the activity in 13.2 TG to avoid any duplication of effort.
- Note 3: ATM = Advanced Technology Microcircuits. Supported by NASA parts bulletin on KGD.
- Note 4: VID = Vendor Item Drawing. Contact DLA for latest information.
- Note 5: ***The boundaries separating various classes/grades must be clearly defined - future outreach activity.***

# Standard RH/RT PEMs for Space

## “Taking SAE AS6294 to the Finish Line”

- SAE CE-12 spent considerable effort in developing a PEM flow for space.
  - Developed SAE AS6294, Requirements for Plastic Encapsulated Microcircuits.
    - ❖ /1 for space, /2 for terrestrial.
- The SAE AS6294 baselined
  - NASA documents
    - ❖ MSFC-STD-3012, GSFC EEE-INST-002, GSFC PEMS-INST-001
  - And, SAE SSB-001
- However, it **never became a standard QML flow**
- Lately, considerable interest in the use of standard plastic parts in space
  - Mainly being driven by **power management** applications
    - ❖ Performance, size, weight advantages; slight cost advantage
    - ❖ Some applications: Cubesats, smallsats, science instruments
    - ❖ New emerging market, does not affect the demand for QMLV products
  - Was discussed on NEPAG (Domestic and International) and GWG telecons
    - ❖ We decided to take a fresher look - what would it take for the SAE AS6294 to become a standard PEMS flow for Space.
  - Several manufacturers offering products built to a flow similar to AS6294
  - Actions:
    - ❖ **JC13.2 to vote on a TG (September 2020)**
      - T.I. and Boeing to co-lead
      - NEPAG government working group (GWG) to provide support
- **Update (December 18, 2020):**
  - JC-13.2 opened a new Task Group, chaired by S. Williams (T.I.) and R. DeLeon (Boeing).
  - NASA published two parts bulletins on PEMS.
  - DLA assigned a new class letter “P” to standard PEMS in Space.
  - **Goal is for NASA and other agencies/users to be able to procure standard (QMLP) parts for use in space applications without having to worry about upscreaming commercial plastic parts.**

## What if A New Product Didn't Fit Any of the Existing Classifications? The "Class Y" Initiative

- It was recognized by the community that packaging and device technology advances are happening rapidly.
- In order to enable space flight projects to benefit from the newly developed devices, e.g., Xilinx Virtex-4 and -5 FPGAs (which are ceramic-based flip-chip nonhermetic parts), a new class was needed.
- NASA led a CE-12 initiative, called Class Y, for infusing Xilinx FPGAs and other similar devices into military/space standards.
- Such an effort must be coordinated with the suppliers and users.
- Need to address all aspects of packaging configuration.
- New test methods must be created and the existing standards updated as necessary.

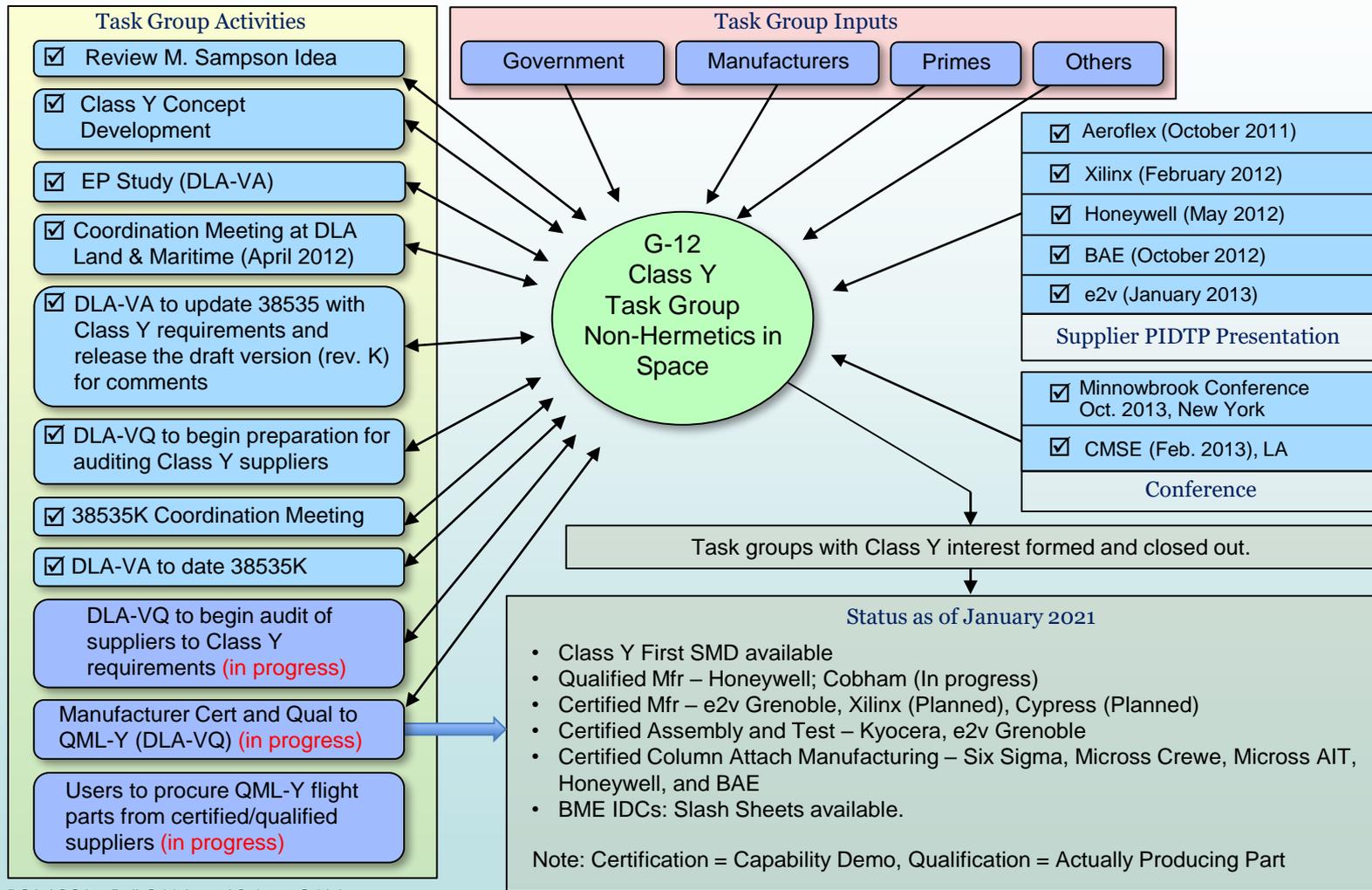
# Class Y, A New Beginning for New Technology Infusion

- **ClassY**

- It represents advancements in packaging technology, increasing functional density, and increasing operating frequency. These are ceramic based single-die system-on-a-chip (SoCs) with non-hermetic flip-chip construction, in high-pin-count ceramic column grid array (CGA) packages. These products use tiny base electrode metal (BME) capacitors for signal integrity, and vented packages for thermal management. (e.g., Xilinx Virtex-4 FPGAs)
- To address the manufacturability, test, quality, and reliability issues unique to new non-traditional assembly/package technologies intended for space applications
  - ❖ Introduced a new concept called Package Integrity Demonstration Test Plan (PIDTP) – provided flexibility to manufacturers.
- This initiative resulted in a major overhaul of MIL-PRF-38535, particularly with respect to requirements for flip-chip, underfill, CSAM, column grid arrays, etc. Revision K reflecting these changes was released in December 2013.

- **Started JC-13.7 to address infusion of new technology**

# Infusion of New Technology into the Standards (Ceramic Based) Class Y Status, January 2021



BGA / CGA = Ball-Grid Array / Column-Grid Array  
BME = Base Metal Electrode  
IDC = Inter Digitized Capacitor

PIDTP = Package Integrity Demonstration Test Plan  
SMD = Standard Microcircuit Drawing

# An Example of SMD Boiler Plate Update

TABLE IIA. Electrical test requirements.

Line Number	Test requirements	Subgroups (in accordance with MIL-PRF-38535, table III)	
		Device class Q	Device class V
1	Interim electrical parameters (see 4.2)	1,2,3,7,8A,8B,9,10,11 <u>1/</u>	1,2,3,7,8A,8B,9,10,11 <u>1/</u>
2	Static burn-in I and II (method 1015)	Not required	Required
3	Same as line 1	---	1, 7 $\Delta$ <u>1/</u> <u>2/</u>
4	Dynamic burn-in (method 1015)	Required	Required
5	Same as line 1	1, 7 $\Delta$ <u>1/</u> <u>2/</u>	1, 7 $\Delta$ <u>1/</u> <u>2/</u>
6	Final electrical parameters	1,2,3,7,8A,8B,9,10,11 <u>1/</u>	1,2,3,7,8A,8B,9,10,11 <u>1/</u>
7	Group A test requirements <u>3/</u>	1,2,3,4,7,8A,8B,9,10,11 <u>4/</u>	1,2,3,4,7,8A,8B,9,10,11 <u>4/</u>
8	Group C end-point electrical parameters <u>3/</u>	1,2,3,7,8A,8B,9,10,11 $\Delta$ <u>2/</u>	1,2,3,7,8A,8B,9,10,11 $\Delta$ <u>2/</u>
9	Group D end-point electrical parameters <u>5/</u>	2,3,8A,8B	2,3,8A,8B
10	Group E end-point electrical parameters <u>3/</u>	1,7,9	1,7,9
11	Column attach <u>6/</u>	1,7,9	1,7,9

- For Flip-chip column attach
  - Add room temperature electricals (subgroups 1, 7, 9) after column attach – step 11 above

# Class Y Qualification Status

- Honeywell Aerospace - Plymouth
  - Complete
  - SMD 5962-17B01
  - Title: Microcircuit, Ceramic Non-Hermetic, Flip Chip, Digital, CMOS SOI, Gate Array, HX5000, Radiation Hardened, Monolithic Silicon
- Cobham Colorado Springs
  - In progress
  - SMD 5962-17B02
  - Tentative Title: Microcircuit, Digital, Radiation Hardened, 90nm Standard Cell, Monolithic Silicon, Class Y, Radiation Hardened, Monolithic Silicon
- Teledyne e2V Grenoble France
  - In progress
  - SMD 5962-19205
  - High performance processor, PC8548
- Cypress Semiconductor
  - Planned
  - 144 Mbit QDR IV SRAM

# Class Y Moving Forward

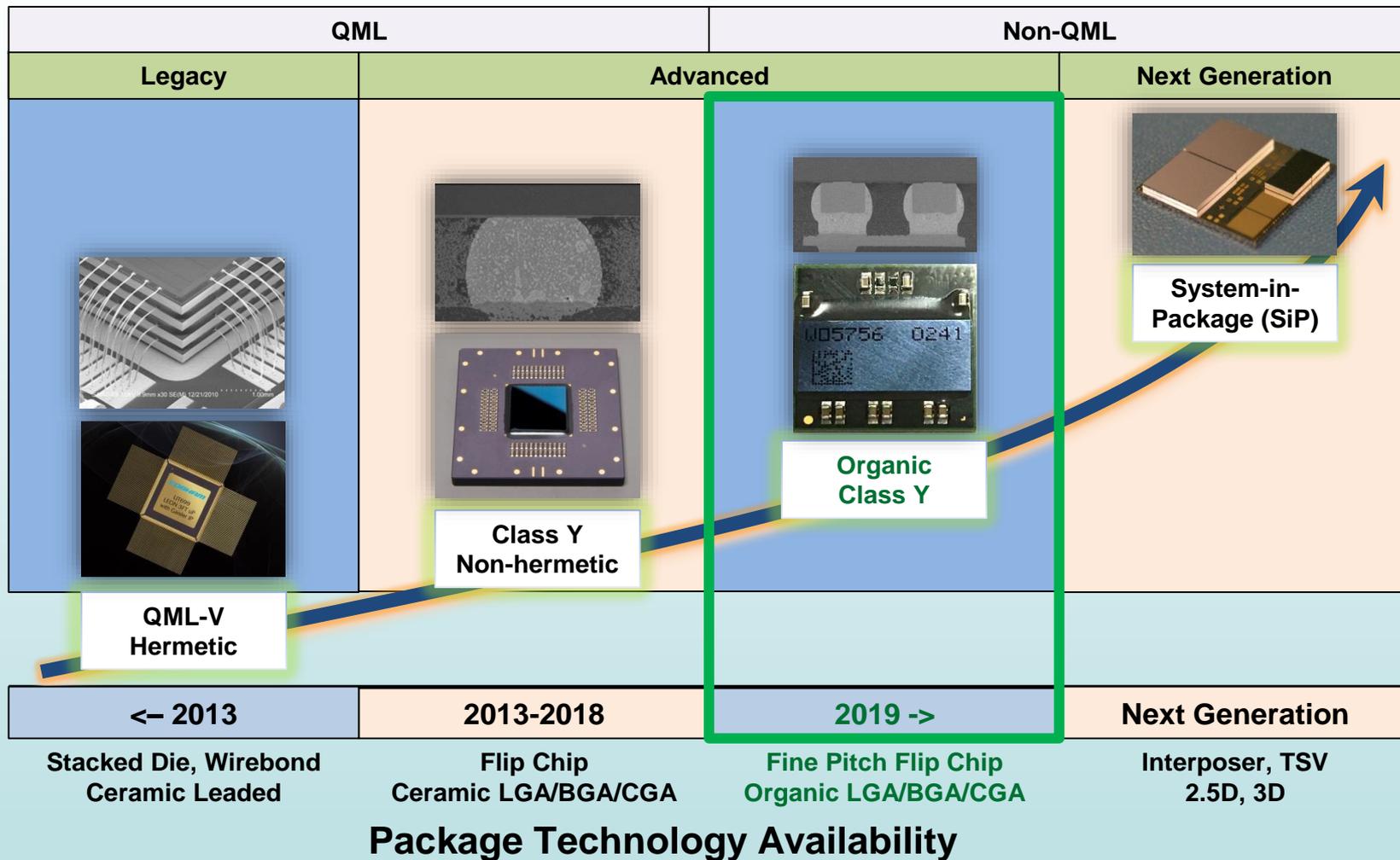
- A Follow-on to Ceramic Substrate Class Y
  - Interest in organic Class Y, and molded plastic parts had been growing.
  - The JC-13.7 created a new task group on organic substrate Class Y (September 2018).
  - Related task groups started as well (next slide)
- Defense Logistics Agency (DLA) conducted an EP (Engineering Practice) study



A test version of Orion

# Next Generation Package Technology for Space Development Roadmap for Space Applications

Performance Requirements



## Package Technology Availability

Credit: Scott Popelar, Cobham, 2019 MRQW, February 7, 2019

# MIL-PRF-ATM (DLA Proposal)

Background: MIL-PRF-38535 offered traditional hermetic class Q and V (class level B, S) and non-hermetic class N and Y devices for military, terrestrial, avionics and space applications. Design requirements of modern electronic satellite/warfare systems are growing faster and moving forward with newer advanced technologies. Considering the complexity of new technologies and device packaging (i.e. 2.5D, 3D type devices) techniques, the current MIL-PRF-38535 may not be the best requirements platform to accommodate for manufacturing these complex and advanced new technology devices.

Accordingly, to bring advancement and adoption of new technologies into the QML system, DLA Land and Maritime is proposing to create a new performance specification, MIL-PRF-ATM applying the Package Integrity Demonstration Test Plan (PIDTP) process to the entire microcircuit manufacturing process. This process was developed for class Y flip chip packages and is successfully used in MIL-PRF-38535 PIDTP requirements.

A JC13.7 task group has been formed to develop the requirements for MIL-PRF-ATM.

ATM = Advanced Technology Microcircuits

# MIL-PRF-ATM

- ATM Devices include:
  - Flip-chip 2.5D, 3D
  - System In Package (SIP)
  - Multi Chip Module (MCM)
- ATM Devices class and application environment:
  - Class M for military(terrestrial and avionics) application
  - Class S for Space application

# Burn-in, and Life Test Comments from NASA

- 1. The regression tables need a fresher look
  - NASA computations show a large variation in the activation energies ( $E_a$ ). See summary below
- 1a. Regression Table in MIL-STD-883, Test Method 1005
  - For Class B,  $E_a$  range = 0.971eV to 0.986eV
  - For Class S,  $E_a$  = 0.292eV to 0.403eV
  - Considerable variation in  $E_a$  values
  - For currently quoted  $E_a$  of 0.7eV
    - ❖ Class B is less conservative
    - ❖ Class S is more conservative
- 1b. Regression Table in MIL-STD-883, Test Method 1015
  - For Class B,  $E_a$  = 0.397eV to 0.409eV
  - For Class S,  $E_a$  = 0.383eV to 0.403eV
  - Considerable variation in  $E_a$  values
  - For currently cited  $E_a$  of 0.7eV
    - ❖ Both Class B and Class S are more conservative
- 1c. What is the correct  $E_a$  going forward?
  - Different sources list different values. According to one source:
    - ❖ 0.3eV is for oxide/dielectric defects, chemical/galvanic/electrolytic corrosion
    - ❖ 0.7eV covers electromigration, broken bonds, lifted die
    - ❖ 1.0eV is for surface contamination induced shifts, lifted bonds (Au-Al interface)
- 2. For accelerated temperature burn-in, and life test
  - ❖ Are the parts characterized for safe operation before they are subjected to elevated temperatures?
  - ❖ Recommend making it a requirement
- 3. JEP 163 Document
  - ❖ Is there a plan to update this document?
- Credits: (1) S. Agarwal, A. Hanelli, M. Han, D. Gallagher, N. Ovee, S. Khandker, R. Evans of NASA/JPL - Cal Tech (2) Subject discussion in 12 Aug, 2020 NASA Electronic Parts Assurance Group (NEPAG) telecon.

# Some Notes on Fracture Mechanics in Plastic Packages

- PEMs
  - Lots of JC13/CE-12 activity to develop Standards for Microcircuits
    - ❖ Heavy discussion on plastic parts in the next 2-3 years (and beyond)
    - ❖ Both ends of the spectrum: overmolded, and organic
    - ❖ Now is a good time to review the fundamentals of plastic packages – the community is making heavy investment in them to cover expanded application spectrum/ infuse new technology
  - Temp cycling
    - ❖ Done per MIL-STD-883, Test Method 1010
      - Condition C: -65C to +150C, used for ceramic parts
      - Condition B: -55C to +125C, being proposed for PEMs for Space
      - Condition A: -55C to +85C
      - How about the ramp rates, dwell times?
  - Glass Transition Temperature
    - ❖ No one seems to talk about it any more, **has been a mystery**
      - **Always measured lower than specified (JPL experience from several years ago)**
  - Packages are getting smaller, thinner
    - ❖ A GaN device that NASA/JPL wants to use, comes in a 8mm x 8mm size package
  - Post Assembly
    - ❖ Are any parts issues (e.g., crack propagation) off limits (IPC problem?)
      - ❖ **CTE mismatches**
      - ❖ **Time dependence**
    - ❖ (Ceramic) SMD-.5 packages had problems at temp cycling after they were mounted on boards
      - Would plastic parts be worse?
    - ❖ Bring parts, IPC, manufacturer communities together
      - **Could a QCI type test/set of guidelines be developed at the part level?**
      - Look at 38535 and 19500 products
  - What tests do the materials suppliers run to demonstrate quality/reliability?
  - Making improvements to standards, performance specifications
    - ❖ **Is the potential impact of stress/pressure build up in plastic packages being adequately addressed?**
- Is it time to address Fracture Mechanics and Microcircuit Standards?
  - To identify any gaps and assess their impact
  - Plastic encapsulants, dielectric polymers, and underfill materials are subject to delamination and cracking with thermal cycling. Crack propagation during use environment exposure, drives the potential for failure of microelectronic devices and is therefore a necessary focal point in qualification and life testing.
  - Develop methodology for evaluating the time-dependent mechanical failure of semiconductor packages
    - ❖ Resulting from combined effect of stress, temperature, moisture absorption and crack like defect

# Applying Fracture Mechanics to PEMs Qualification - Key Points

(J. Evans, NASA)



- Fracture is a critical reliability issue for the packaging
  - Cracking of enclosure
  - Delamination
  - Cracking of polymer passivation
- Fracture mechanics can inform our testing
- Most critical stresses occur in assembly: greatest opportunity for defect formation
  - Thermomechanical
  - Hydromechanical
- Moisture control and handling of packages of critical importance
- Screening by Thermomechanical Loading Imposes Risk
- Defect propagation may occur post assembly in thermomechanical loading
- Risk increases with complex packaging

## Conclusion

- New technology infusion is an on-going challenge.
- NASA supports a wide spectrum of space missions/programs ranging from smallsats/cubesats to flagship missions such as Juno and the planned Europa mission. The success of each mission is important.
- NASA is working with the space community to help infuse new technologies into the military standards. ESD aspects should not be ignored. We encourage the world wide space community to get/stay involved in developing/updating standards.

Thank you!

# BACKUP MATERIAL

# NASA EEE Parts Bulletin, May 15, 2020



October 2019–March 2020 • Volume 11, Issue 1,<sup>1</sup> May 15, 2020

## Non-Hermetic and Plastic-Encapsulated Microcircuits

The mission assurance organizations at NASA have supported many large and small space missions and programs over the years. Today that spectrum has expanded, ranging from flagship missions such as Mars 2020 with its Perseverance Rover, Europa Clipper, and the proposed Europa Lander, to SmallSats/CubeSats such as the Temporal Experiment for Storms and Tropical Systems—Demonstration (TEMPEST-D) and Mars Cube One (MarCO). Plastic-encapsulated microcircuits (PEMs) have become more attractive since leading-edge alternatives are not available as space-qualified products. PEMs generally have smaller footprints and are lighter than the ceramic packages used in space-qualified products [1]. As the demand and use of non-hermetic and plastic-encapsulated microcircuits for space has increased, the scope of what future missions are capable of has also widened. This changing climate related to EEE parts selection presents new challenges for NASA, which—as always—holds the success of every mission paramount.

## Growing Use of NASA SmallSats and CubeSats

Due to the need for low-cost communications satellites and new businesses evolving around Earth-observation services, there's been an increased interest in the use of CubeSats and SmallSats. Many NASA centers have been involved in developing and flying CubeSats and SmallSats, working together with multiple universities and industry partners. These undertakings require new product solutions for smaller, lighter, and lower-cost spacecraft, which cannot be produced using traditional space-qualified electronic parts.

The reliability and radiation requirements for CubeSats and SmallSats are significantly lower than for larger spacecraft because these smaller satellites operate mainly in low Earth or geosynchronous orbits (LEO or GEO, as opposed to deep space) and for relatively short periods. Radiation-hardened, high-reliability, space-grade parts are often too expensive for such missions and do not match well with their requirements.

There are a few notable exceptions to the usual use of CubeSats, particularly MarCO-A and MarCO-B, which were the first CubeSats to fly to deep space, where they successfully supported the Interior Exploration Using

Seismic Investigations, Geodesy, and Heat Transport (InSight) mission by relaying data to Earth from Mars during the entry, descent and landing stage (Figure 1). MarCO successfully demonstrated a "bring-your-own" communications-relay option for use by future Mars missions in the critical few minutes between Martian atmospheric entry and touchdown. Further, by verifying that CubeSats are a viable technology for interplanetary missions, and feasible on a short development timeline, this technology demonstration could lead to many other applications to explore and study our solar system.

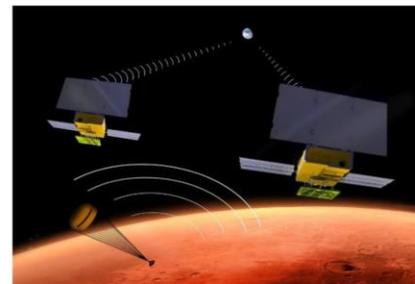


Figure 1. MarCO accompanying the InSight Mars lander and relaying data to Earth as it landed on Mars.

<sup>1</sup> The EEE Parts Bulletin was not published in fiscal year 2019 (FY19). The two issues of Volume 10 were published in FY18.

# NASA EEE Parts Bulletin

## Special Edition: Non-Hermetic and Plastic-Encapsulated Microcircuits, Part 2

### URS296932, CL#20-6169



#### Non-Hermetic and Plastic-Encapsulated Microcircuits, Part 2

The mission assurance organizations at NASA have supported many large and small space missions and programs over the years. Today, that spectrum has expanded, ranging from flagship missions such as Mars 2020 with its Perseverance Rover, Europa Clipper, and the proposed Europa Lander, to SmallSats/CubeSats such as the Temporal Experiment for Storms and Tropical Systems—Demonstration (TEMPEST-D) and Mars Cube One (MarCO). Plastic-encapsulated microcircuits (PEMs) have become more attractive since leading-edge alternatives are not available as space-qualified products. PEMs generally have smaller footprints and are lighter than the ceramic packages used in space-qualified products [1]. As the demand for and use of non-hermetic and plastic-encapsulated microcircuits for space has increased, the scope of what future missions are capable of has also widened. This changing climate of EEE parts selection presents new challenges for NASA, which—as always—holds the success of every mission paramount. In this second issue devoted to non-hermetic and plastic-encapsulated microcircuits, we discuss more manufacturers' PEMs flows, and introduce the AS6294/1 aerospace standard document on "Requirements for Plastic Encapsulated Microcircuits in Space Applications."

#### Aerospace Standard AS6294/1

Due to the need for low-cost communications satellites and for new businesses evolving around Earth-observation services, there's been increased interest in the use of CubeSats and SmallSats for such missions. Many NASA centers have been involved in developing and flying CubeSats and SmallSats, working with multiple universities and industry partners. These undertakings require new product solutions for smaller, lighter, and lower-cost spacecraft that cannot be produced using traditional space-qualified products.

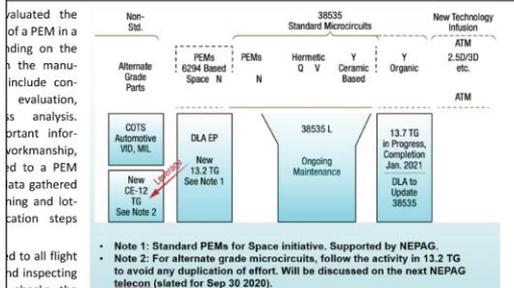
In 2017, a subcommittee of SAE International's Group 12 (G12) was created to standardize a PEMs flow and to address a possible future extension of the Qualified Manufacturer List (QML) system to include PEMs for space. Considerable effort was put into developing a PEMs flow for space applications, documented in SAE Aerospace Standard AS6294/1, issued in November 2017, titled "Requirements for Plastic Encapsulated Microcircuits in Space Applications." The "1" version was directed at space applications, the "2" version at

terrestrial applications. SAE AS6294/1 pulled information from many Marshall Space Flight Center (MSFC), Goddard Space Flight Center (GSFC), and SAE standards applicable to NASA—namely, MSFC-STD-3012, GSFC EEE-INST-002, GSFC PEMS-INST-001, and SAE SSB-001—as well as reviews of multiple industry practices.

AS6294/1 defines the requirements for screening, qualification, and lot-acceptance testing for use of PEMs in space flight applications. The level of testing is dependent on the risk approach, the application, and the reliability and radiation requirements of the mission. However, AS6294/1 contains only requirements that meet the highest known reliability for space applications. The document also addresses many concerns associated with PEMs, such as narrower operating temperature ranges and greater susceptibility to infant mortality and moisture absorption than space-grade products have [2]. AS6294/1 starts with device characterization for parts that don't meet space requirements. The characterization step includes the initial investigations needed to understand the details of the technology used in a PEM product [2]. This is crucial when the

<sup>1</sup> This issue is a follow-on to Volume 11, Issue 1, released May 15, 2020: "Non-Hermetic and Plastic Encapsulated Microcircuits."

Once the task group based on JC13.2 completes its work, a new proposed TG will be formed to support alternate-grade microcircuits. The work performed by the JC13.2



- Note 1: Standard PEMs for Space initiative. Supported by NEPAG.
- Note 2: For alternate grade microcircuits, follow the activity in 13.2 TG to avoid any duplication of effort. Will be discussed on the next NEPAG telecon ( slated for Sep 30 2020).

Figure 1. Options for standard, nonstandard, and new-technology microcircuits.

evaluated the of a PEM in a nding on the h the manu- include con- evaluation, s analysis. portant infor- workmanship, d to a PEM ata gathered hing and lot- cation steps

nd to all flight nd inspecting checks in the screening test in AS6294/1.

nd functional tests, a percent value is calculated with a

performed on parts that pass h step includes life-testing, temperatures, temperature d by failure analysis for any ts have met all requirements ever beared for flight.

ever become a standard QML immediately adopted in its nufacturers, who offer their that in AS6294/1. With the use of standard plastic ns, the space community ment and take a renewed a standard PEMs flow for icussed in domestic and nic Parts Assurance Group t Working Group (GWG) open a new task group was 20 JC13.2 session, in which task group from industry SWG support. The task group manna Williams of Texas leon of Boeing.

TG will be heavily leveraged in order to avoid any duplication of effort. See Figure 1 for details on current and future options for nonstandard, standard, and new-technology microcircuits.

#### Manufacturer Solutions for Non-Hermetic and Plastic-Encapsulated Microcircuits

Historically, satellite programs have used space-grade, hermetically sealed, QML-V (space) and QML-Q (military) qualified components for enhanced reliability and radiation hardness. With the emergence of "commercial space," there has been increased interest in using PEMs in space for a variety of reasons. Countering the concerns cited above—narrow operating temperature ranges and susceptibility to infant mortality and moisture absorption [2]—are certain advantages of PEMs over most space-grade hermetically sealed microcircuits: lower cost and weight, more advanced performance, lower power consumption, and smaller overall package size.

With this new growing trend in the market, an increasing number of suppliers now offer a wide range of enhanced plastic product solutions depending on quality, reliability, radiation, and cost. Not all of these product lines follow a consolidated test flow, and all depend on the specific tailoring that each manufacturer makes to them. Hopefully, in the near future, the industry will lean

mon flow that will be produced S.

develops and manufactures ts for healthcare, life sciences, efense, security, and industrial her ceramic and plastic, hermetic ns, tested to various flows, -Q, QML-Y (non-hermetic for more. Table 1 shows Teledyne nd qualification flows and the they use [3].

space applications, sub-QML e arrays (FPGAs) aimed at in traditional QML components shelf (COTS) components, the radiation or reliability data. For ns and constellations of small tringent cost and schedule FPGAs are the optimal solutions, tolerance of QML components flight heritage, which permits

reduced screening requirements, resulting in reduced cost and lead times.

Microchip also provides two space plastic flows: HiRel plastic radiation-tolerant (HP) and 8-lead plastic small-outline (SN). The HP flow is for low-cost and high-volume requirements, typically meeting low-Earth-orbit (LEO) constellations' needs. The SN flow provides a higher screening level, including wafer lot acceptance, serialization, 100% thermal cycling, 100% burn-in, and PDA. These flows apply to both rad-hard-by-design and rad-tolerant products. Products made to these flows (SN, HP) meet qualification levels compliant with automotive requirements (AEC-Q100), with the SN flow based on AS6294/1. See Table 2 for more details on the screening and qualification flows for Microchip HP and SN devices [4].

Microc offers an extensive array of COTS components—both hermetic and plastic—including a wide selection of power modules and small-signal discretes. They also stock a wide range of upscreened plastic products, including an assortment of integrated PEM (IPEM) memory devices that have been tested to selected high-reliability performance levels. In their Retail+ products line, Microc provides customers with industry-leading

Table 1. Teledyne e2v has various plastic non-hermetic test flows.

Benefits	"Nix" NASA level			
	Level 1	Level 2	Level 3	Enhanced
Specification reference -->>	EEE-INST-002	PEM-INST-001		EP Int. procedure
Assembly and test site, one BOM	✓	✓	✓	✓
Datalog	✓	✓		
Condition/method				
MIL-STD-883 TM1010 cond. B (55/125°C) or C (45/150°C)	20 cys - cond.	B20 cys - cond.	B20 cys - cond.	B10 cys - cond.
MIL-STD-883 TM20012		✓		
Radiate				
MIL-STD-883 TM1015 cond. O (125°C)	240 hrs	160 hrs	160 hrs	160 hrs
MIL-STD-883 TM1015 conds. A, B or C (125°C)	120 hrs			
Electricals				
Moisture soak/Reflow simulation	5%	10%	10%	5%
Ambient temperature post dynamic	✓	✓	✓	✓
Per device specification (55/125°C)	✓	✓	✓	✓
Per device specification (25°C)	✓	✓	✓	✓
SA) Condition/method				
TID & SEE	Per rad tests	Per rad tests	Per rad tests	
PEM-INST-001	22	22		
Moisture soak/Reflow simulation	32	32	17	
MIL-STD-883 TM1005 / O / 125°C	1500 hrs / 22	1000 hrs / 22	500 hrs / 10	
MIL-STD-883 TM1010 / B + DPA	500 cys / 22	200 cys / 22	100 cys / 10	
PEM-INST-001	22	22		
EEE-INST-002 on 5 parts	✓	✓		
PEM-INST-001	10			
Sub-group 1b - DPA/FA				
Sub-group 2 - Biased HAST				
Sub-group 2 - Unbiased HAST				



# Texas Instruments (TI)

## Space EP Baseline Controlled Flow

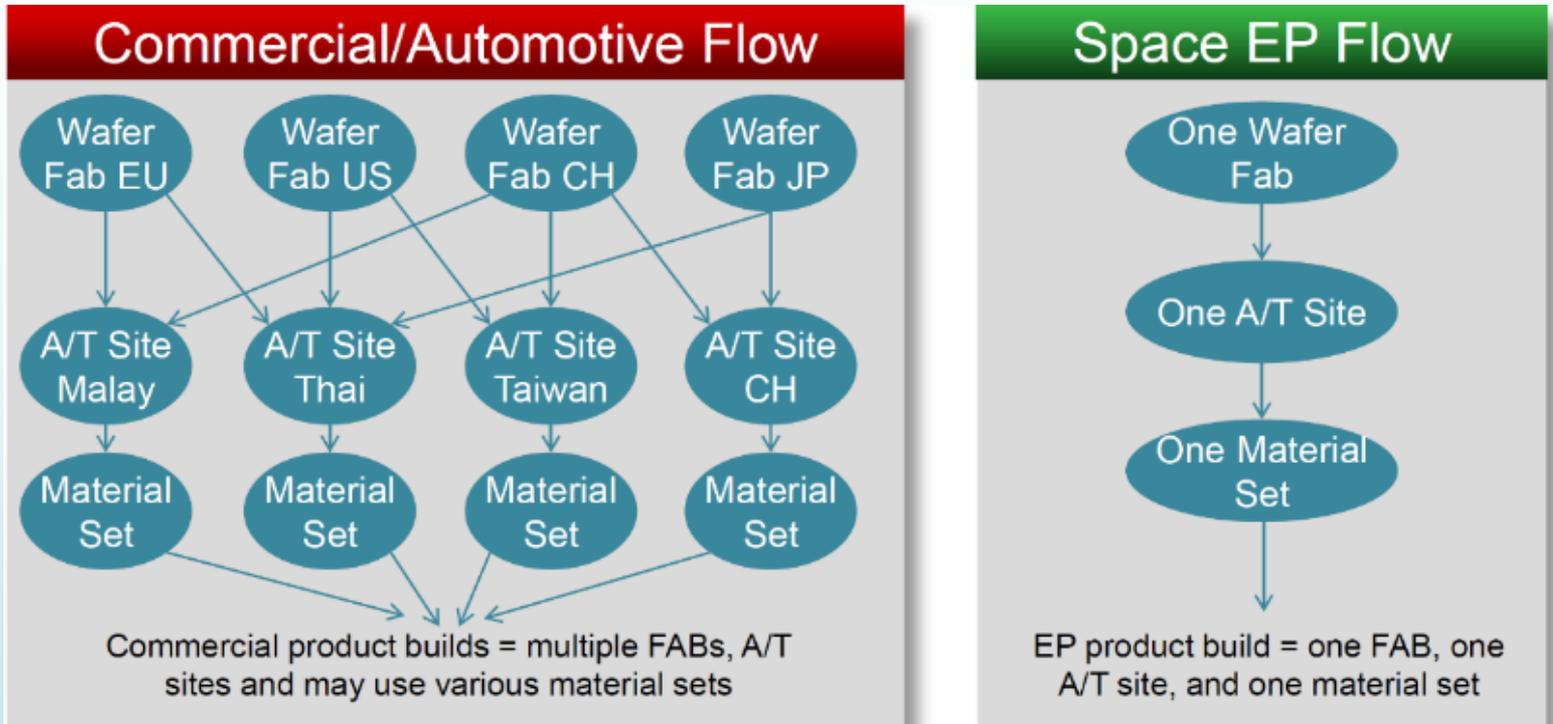


Image Courtesy of Texas Instruments

- The above chart provided by TI shows that their commercial/automotive products maybe built at multiple foundries, assembly/test facilities and may use various material sets.
- Contact manufacturer for a current version of this chart.

# <http://nepp.nasa.gov>



## **ACKNOWLEDGMENTS**

The research described in this publication was carried out, in part, at the Jet Propulsion Laboratory, California Institute of Technology, under a contract with the National Aeronautics and Space Administration. Help is gratefully acknowledged from Mohammad Mojjaradi, Jeremy Bonnell, Brandon Bodkin and Joon Park.

Government sponsorship acknowledged.